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[APPENDIX G - SHORT CIRCUIT ANALYSIS RESULTS](#)

Appendix GShort Circuit Analysis Results BREAKER Data COD Pre COD Post Overduty Violation FY Pre FY Post Overduty Violation23002A Sum of DUTY P 95.8 96.3 FALSE 101.7 102.1 FALSE Sum of BKR CAPA 42000 42000 42000 42000BK72 Sum of DUTY P 95.8 96.3 FALSE 101.7 102.1 FALSE Sum of BKR CAPA 42000 42000 42000 42000CW69 Sum of DUTY P 8.5 8.5 FALSE 8.4 8.4 FALSE Sum of BKR CAPA 32000 32000 32000 32000DI65...

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[SEQUENTIAL CIRCUIT ANALYSIS](#)

Sequential Circuit Analysis 1 Mealy or Moore??? The output of a sequential circuit can be expressed in two different ways: Moore model: Outputs= f(present state) Mealy model: Outputs: f(present state, inputs) Inputs Combinational Outputs circuit Memory 2 ...

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